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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re application of:  
Bhatt et al.

Serial No. 09/778,702

Filed: February 7, 2001

For: **Full Additive Process with Filled Plated  
Through Holes**

) Examiner: Jose H. Alcala  
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) Art Unit: 2841  
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Attorney Docket No. END919960141US2 (IEN-10-5222-D1)

AMENDMENT

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Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

NOV 29 2001

TECHNOLOGY CENTER 2800

Dear Sir:

Applicants are herewith filing an amendment responsive to the examiner's letter of August 29, 2001. It is respectfully requested that this application be amended as follows:

**In the Drawings:**

The accompanying drawings have been corrected pursuant to the comments of the examiner in his letter of August 29, 2001 and in the examiner interview conducted with the undersigned on November 28, 2001.

**In the Specification:**

Kindly amend the specification as follows:

In the Abstract, kindly insert the following at the beginning of the first line:

--The present invention provides a printed circuit board and a method for the production of a printed circuit board having fine-line circuitry and greater aspect ratio on a subcomposite with plated through holes. --

END919960141US2 (IEN-10-5222-D1)